Guest Editor's Overview

THE 2001 IEEE Radio Frequency Integrated Circuit (RFIC) Symposium was held in Phoenix, AZ, during the same week as the IEEE International Microwave Symposium, May 20–May 22. The RFIC Symposium has gained momentum in the last few years as the outlet of technology presentations for commercial RFIC developments for the wireless and wired communications industries. As the technologies advance, we can see higher integration and complexity of these RFICs that include RF/analog/digital circuitry that represent the radio or a chip on the system-on-chip (SOC) concept. The diverse knowledge required to put such complex chips together forms the essence of the RFIC community that attends the RFIC symposium. This TRANSACTIONS' Mini-Special Issue is a collection of outstanding papers that have been extended beyond the conference digest.

I would like to acknowledge the conscientious and timely response of each of the reviewers listed to the right. It is through their efforts that the IEEE RFIC Symposium and the IEEE Microwave Theory and Techniques Society (IEEE MTT-S) is able to continue to maintain the quality and high standards of this TRANSACTIONS.

The reviewers of this TRANSACTIONS' Mini-Special Issue are as follows:

L. Boglione
T. Cho
Y. Deval
S. Heinen
A. Jerng
R. Kagiwada
S. Kiaei
B. Kuhn
M. Kumar
S. Lloyd
D. Lovelace
D. Ngo
J. Staudinger

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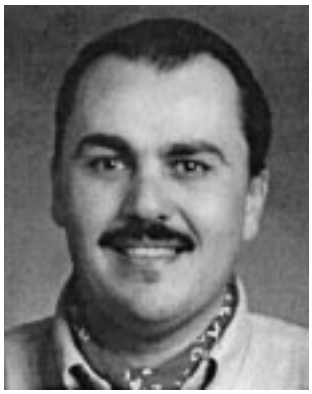

Natalino Camilleri (S'79–M'80–SM'93) received the B.S.E.E. degree from the University of Malta, Malta, and the M.S.E.E. and Ph.D. degrees from the University of Texas at Austin.

He possesses over 20 years of experience in the development and creation of RF systems and integrated circuits. He is currently the President of RF & Wireless Design Services Inc., Cupertino, CA, a consulting company performing RFIC design. Earlier in his career, he started up the Radio Product Line at Advanced Micro Devices, and was able to take some early product ideas and develop the award-winning chip set for digital spread-spectrum (DSS) cordless phones. He led the group through an acquisition by DSPG and was the President of RF Integrated Systems Inc. He was instrumental in the development of the 900-MHz giga-range phones by Panasonic that use DSS chip sets. At AMD, he also developed integrated circuits (ICs) for wireless local-area network (WLAN) and GSM applications. Prior to his involvement with AMD, he was the Silicon RF Technology Manager at Motorola, where he developed leading-edge circuits for new technologies such as SiGe, BiCMOS, LDMOS, and CMOS. He was instrumental in developing the LDMOS discrete technology and was the founding father for the LDMOS IC technology for Motorola. These products are now the premiere technology for Motorola's RF business. He originally joined Motorola to start up the GaAs operation as the Design and Application Manager. He was instrumental in kicking off the early technology and IC products that later fueled GaAs RFIC business for Motorola. Prior to Motorola, he was with HP-Avantek and Texas Instruments Incorporated, where he was a Senior Member of the Technical Staff and developed ICs for microwave and millimeter-wave applications. He has authored or co-authored over 50 papers concerning RF technology.

Dr. Camilleri has been on several IEEE Microwave Theory and Techniques Society (IEEE MTT-S) Technical Program Committees (TPCs) for 15 years and has held several positions on the RFIC Symposium Steering Committee.